# Design of FPGA-based Sliding Mode Controller for Robot Manipulator

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#### Abstract

One of the most active research areas in the field of robotics is robot manipulators control. because these systems are multi-input multi-output (MIMO), nonlinear, and uncertainty. At present, robot manipulators is used in unknown and unstructured situation and caused to provide complicated systems, consequently strong mathematical tools are used in new control methodologies to design nonlinear robust controller with satisfactory performance (e.g., minimum error, good trajectory, disturbance rejection). Robotic systems controlling is vital due to the wide range of application. Obviously stability and robustness are the most minimum requirements in control systems; even though the proof of stability and robustness is more important especially in the case of nonlinear systems. One of the best nonlinear robust controllers which can be used in uncertainty nonlinear systems is sliding mode controller (SMC). Chattering phenomenon is the most important challenge in this controller. Most of nonlinear controllers need real time mobility operation: one of the most important devices which can be used to solve this challenge is Field Programmable Gate Array (FPGA). FPGA can be used to design a controller in a single chip Integrated Circuit (IC). In this research the SMC is designed using VHDL language for implementation on FPGA device (XA3S1600E-Spartan-3E). with minimum chattering and high processing speed (63.29 MHz).

**Keywords**: Robot Manipulator, Sliding Mode Controller, Chattering Phenomenon, FPGA, VHDL language.

## 1. INTRODUCTION

A robot is a machine which can be programmed as a reality of tasks which it has divided into three main categories i.e. robot manipulators, mobile robots and hybrid robots. PUMA 560 robot manipulator is an articulated 6 DOF serial robot manipulator. This robot is widely used in industrial and academic area and also dynamic parameters have been identified and documented in the literature. From the control point of view, robot manipulator divides into two main sections i.e. kinematics and dynamic parts. Estimate dynamic parameters are considerably important to control, mechanical design and simulation[1].

Sliding mode controller (SMC) is one of the influential nonlinear controllers in certain and uncertain systems which are used to present a methodical solution for two main important controllers' challenges, which named: stability and robustness. Conversely, this controller is used in different applications; sliding mode controller has subsequent drawbacks i.e. chattering phenomenon, and nonlinear equivalent dynamic formulation in uncertain systems[1-2].

In order to solve the chattering in the systems output, boundary layer method should be applied so beginning able to recommended model in the main motivation. Conversely boundary layer method is constructive to reduce or eliminate the chattering; the error response quality may not always be so high. Besides using boundary layer method in the main controller of a control loop, it can be used to adjust the sliding surface slope to have the best performance (reduce the chattering and error performance)[3].

Commonly, most of nonlinear controllers in robotic applications need a mobility real time operation. FPGA-based controller has been used in this application because it is small device in size, high speed, low cost, and short time to market. Therefore FPGA-based controller can have a short execution time because it has parallel architecture [4-7].

#### This paper is organized as follows:

In section 2, main subject of modelling PUMA-560 robot manipulator formulation are presented. Detail of classical sliding mode controller is presented in section 3. In section 4, the main subject of FPGA-based sliding mode controller is presented. In section 5, the simulation result is presented and finally in section 6, the conclusion is presented.

## 2. DYNAMIC FORMULATION OF ROBOT

It is well known that the equation of an *n-DOF* robot manipulator governed by the following equation [1-2]:

$$M(q)\ddot{q} + N(q,\dot{q}) = \tau$$

Where **T** is actuation torque, M(q) is a symmetric and positive define inertia matrix,  $N(q, \dot{q})$  is the vector of nonlinearity term. This robot manipulator dynamic equation can also be written in a following form:

$$\tau = M(q)\ddot{q} + B(q)[\dot{q}\,\dot{q}] + C(q)[\dot{q}]^2 + G(q)$$

Where the matrix of coriolios torque is B(q), C(q) is the matrix of centrifugal torques, and G(q) is the vector of gravity force. The dynamic terms in equation (2) are only manipulator position. This is a decoupled system with simple second order linear differential dynamics. In other words, the component  $\ddot{q}$  influences, with a double integrator relationship, only the joint variable  $q_i$ , independently of the motion of the other joints. Therefore, the angular acceleration is found as to be[2]:

$$\ddot{\boldsymbol{q}} = \boldsymbol{M}^{-1}(\boldsymbol{q}) \{ \boldsymbol{\tau} - \boldsymbol{N}(\boldsymbol{q}, \dot{\boldsymbol{q}}) \}$$

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( 1 )

( 2 ) This technique is very attractive from a control point of view. This paper is focused on the design FPGA-based controller for PUMA-560 robot manipulator.

### 2.1 PUMA 560 Dynamic Formulation

Position control of PUMA-560 robot manipulator is analyzed in this paper; as a result the last three joints are blocked. The dynamic equation of PUMA-560 robot manipulator is given as

$$M(\ddot{\theta})\begin{bmatrix}\ddot{\theta}1\\\ddot{\theta}_{2}\\\ddot{\theta}_{3}\end{bmatrix} + B(\theta)\begin{bmatrix}\dot{\theta}_{1}\dot{\theta}_{2}\\\dot{\theta}_{1}\dot{\theta}_{3}\\\dot{\theta}_{2}\dot{\theta}_{3}\end{bmatrix} + C(\theta)\begin{bmatrix}\dot{\theta}_{1}^{2}\\\dot{\theta}_{2}^{2}\\\dot{\theta}_{3}^{2}\end{bmatrix} + G(\theta) = \begin{bmatrix}\tau_{1}\\\tau_{2}\\\tau_{3}\end{bmatrix}$$
(4)

Where

$$M(q) = \begin{bmatrix} M_{11} & M_{12} & M_{13} & 0 & 0 & 0 \\ M_{21} & M_{22} & M_{23} & 0 & 0 & 0 \\ M_{31} & M_{32} & M_{33} & 0 & M_{35} & 0 \\ 0 & 0 & 0 & M_{44} & 0 & 0 \\ 0 & 0 & 0 & 0 & M_{55} & 0 \\ 0 & 0 & 0 & 0 & 0 & M_{66} \end{bmatrix}$$

Suppose 
$$\ddot{q}$$
 is written as follows  
 $\ddot{q} = M^{-1}(q) . \{ \tau - [B(q)\dot{q}\dot{q} + C(q)\dot{q}^2 + g(q)] \}$ 

and *I* is introduced as  

$$I = \{\tau - [B(q)\dot{q}\dot{q} + C(q)\dot{q}^2 + g(q)]\}$$
(1)

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$$\ddot{q}$$
 can be written as  
 $\ddot{q} = M^{-1}(q). I$ 

Therefore *I* for PUMA-560 robot manipulator can be calculated by the following equation  $I_1 = \tau_1 - [b_{112}\dot{q}_1\dot{q}_2 + b_{113}\dot{q}_1\dot{q}_3 + 0 + b_{123}\dot{q}_2\dot{q}_3] - [C_{12}\dot{q}_2^2 + C_{13}\dot{q}_3^2] - g_1$  0 )

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$$I_2 = \tau_2 - [b_{223}\dot{q}_2\dot{q}_3] - [C_{21}\dot{q}_1^2 + C_{23}\dot{q}_3^2] - g_2$$

$$I_3 = \tau_3 - \left[C_{31}\dot{q}_1^2 + C_{32}\dot{q}_2^2\right] - g_3$$

$$I_4 = \tau_4 - [b_{412}\dot{q}_1\dot{q}_2 + b_{413}\dot{q}_1\dot{q}_3] - g_4$$

$$I_5 = \tau_5 - [C_{51}\dot{q}_1^2 + C_{52}\dot{q}_2^2] - g_5$$

$$I_6=\tau_6$$

### 3. CLASSICAL SLIDING MODE CONTROL

Sliding mode controller (SMC) is a powerful nonlinear controller which has been analyzed by many researchers especially in recent years. This theory was first proposed in the early 1950 by Emelyanov and several co-workers and has been extensively developed since then with the invention of high speed control devices[1-2].

A time-varying sliding surface s(x, t) is given by the following equation:

$$s(x,t) = (\frac{d}{dt} + \lambda)^{n-1} \widetilde{x} = \mathbf{0}$$

where  $\lambda$  is the constant and it is positive. To further penalize tracking error integral part can be used in sliding surface part as follows:

$$s(x,t) = \left(\frac{d}{dt} + \lambda\right)^{n-1} \left(\int_0^t \widetilde{x} \, dt\right) = 0 \tag{(1)}$$

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The main target in this methodology is keep s(x, t) near to the zero when tracking is outside of s(x, t). Therefore, one of the common strategies is to find input U outside of s(x, t).

$$\frac{1}{2}\frac{d}{dt}s^2(x,t) \le -\zeta |s(x,t)|$$

where  $\pmb{\zeta}$  is positive constant.

If 
$$S(0)>0 \rightarrow \frac{d}{dt}S(t) \leq -\zeta$$

To eliminate the derivative term, we used an integral term from t=0 to t= $t_{reach}$ 

$$\int_{t=0}^{t-t_{reach}} \frac{d}{dt} S(t) \leq -\int_{t=0}^{t-t_{reach}} \eta \to S(t_{reach}) - S(0)$$

$$\leq -\zeta(t_{reach} - 0)$$

$$(2)$$

Where  $t_{reach}$  is the time that trajectories reach to the sliding surface so, if we assume that  $S(t_{reach} = 0)$  then:

$$0 - S(0) \leq -\eta(t_{reach}) \rightarrow t_{reach} \leq \frac{S(0)}{\zeta}$$

and

$$if S(\mathbf{0}) < 0 \rightarrow 0 - S(\mathbf{0}) \le -\eta(t_{reach}) \rightarrow S(\mathbf{0}) \le -\zeta(t_{reach}) \rightarrow t_{reach}$$
$$\le \frac{|S(\mathbf{0})|}{\eta}$$

Equation (24) guarantees time to reach the sliding surface is smaller than  $\frac{|S(0)|}{\zeta}$  if trajectories are outside of **S(t)**.

$$if S_{t_{reach}} = S(0) \rightarrow error(x - x_d) = 0$$

suppose S defined as

$$s(x,t) = (\frac{d}{dt} + \lambda)$$
  $\tilde{x} = (\dot{x} - \dot{x}_d) + \lambda(x - x_d)$ 

The derivation of S, namely,  $\dot{S}$  can be calculated as the following formulation:  $\dot{S} = (\ddot{x} - \ddot{x}_d) + \lambda(\dot{x} - \dot{x}_d)$  suppose define the second order system as,

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A simple solution to get the sliding condition when the dynamic parameters have uncertainty is the switching control law:

$$U_{dis} = U - K(\vec{x}, t) \cdot sgn(s)$$

Where the function of *sgn*(*S*) defined as;

$$sgn(s) = \begin{cases} 1 & s > 0 \\ -1 & s < 0 \\ 0 & s = 0 \end{cases}$$

and the  $K(\vec{x}, t)$  is the positive constant. Suppose to rewrite the equation (20) by the following equation,

$$\frac{1}{2}\frac{d}{dt}s^{2}(x,t) = \dot{S}.S = [f - \hat{f} - Ksgn(s)].S$$

$$= (f - \hat{f}).S - K|S|$$
(3)

Another method is using equation (23) instead of (24) to get sliding surface

$$s(x,t) = \left(\frac{d}{dt} + \lambda\right)^2 \left(\int_0^t \widetilde{x} \, dt\right)$$

$$= (\dot{x} - \dot{x}_d) + 2\lambda(\dot{x} - \dot{x}_d) - \lambda^2(x - x_d)$$
(3)

in this method the approximation of U can be calculated as

$$\widehat{U} = -\widehat{f} + \ddot{x}_d - 2\lambda(\dot{\mathbf{x}} - \dot{\mathbf{x}}_d) + \lambda^2(\mathbf{x} - \mathbf{x}_d)$$

To reduce or eliminate the chattering it is used the boundary layer method; in boundary layer method the basic idea is replace the discontinuous method by saturation (linear) method with small neighborhood of the switching surface. This replace is caused to increase the error performance.

$$\boldsymbol{B}(\boldsymbol{t}) = \{\boldsymbol{x}, |\boldsymbol{S}(\boldsymbol{t})| \le \emptyset\}; \emptyset > 0$$

Where Ø is the boundary layer thickness. Therefore, to have a smote control law, the saturation function  $Sat(^{S}/_{\emptyset})$  added to the control law:

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$$U = K(\vec{x}, t). Sat\left(\frac{S}{\phi}\right)$$
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Where  $Sat(S/\phi)$  can be defined as

$$sat(s/\phi) = \begin{cases} 1 & (s/\phi > 1) & (3) \\ -1 & (s/\phi < 1) & (3) \\ s/\phi & (-1 < s/\phi < 1) & (3) \\ (-$$

Based on above discussion, the control law for a multi degrees of freedom robot manipulator is written as: ( 3

$$\hat{\tau} = \hat{\tau}_{eq} + \hat{\tau}_{sat}$$

Where, the model-based component  $\hat{\tau}_{eq}$  is compensated the nominal dynamics of systems. Therefore  $\hat{\tau}_{eq}$  can calculate as follows:

$$\hat{\tau}_{eq} = \left[ M^{-1}(B + C + G) + \dot{S} \right] M$$

Where  

$$\hat{\tau}_{eq} = \begin{bmatrix} \widehat{\tau_{eq1}} \\ \widehat{\tau_{eq2}} \\ \widehat{\tau_{eq3}} \\ \widehat{\tau_{eq4}} \\ \widehat{\tau_{eq6}} \end{bmatrix}, M^{-1} = \begin{bmatrix} M_{11} & M_{12} & M_{13} & 0 & 0 & 0 \\ M_{21} & M_{22} & M_{23} & 0 & 0 & 0 \\ M_{31} & M_{32} & M_{33} & 0 & M_{35} & 0 \\ 0 & 0 & 0 & M_{44} & 0 & 0 \\ 0 & 0 & 0 & 0 & M_{55} & 0 \\ 0 & 0 & 0 & 0 & 0 & M_{66} \end{bmatrix}^{-1}$$

$$B + C + G$$

$$= \begin{bmatrix} b_{112}\dot{q}_{1}\dot{q}_{2} + b_{113}\dot{q}_{1}\dot{q}_{3} + 0 + b_{123}\dot{q}_{2}\dot{q}_{3} \\ 0 + b_{223}\dot{q}_{2}\dot{q}_{3} + 0 + 0 \\ 0 \\ b_{412}\dot{q}_{1}\dot{q}_{2} + b_{413}\dot{q}_{1}\dot{q}_{3} + 0 + b_{123}\dot{q}_{2}\dot{q}_{3} \\ 0 \\ 0 \end{bmatrix}$$

$$+ \begin{bmatrix} C_{12}\dot{q}_{2}^{2} + C_{13}\dot{q}_{3}^{2} \\ C_{21}\dot{q}_{1}^{2} + C_{23}\dot{q}_{2}^{2} \\ 0 \\ C_{51}\dot{q}_{1}^{2} + C_{52}\dot{q}_{2}^{2} \end{bmatrix} + \begin{bmatrix} 0 \\ g_{2} \\ g_{3} \\ 0 \\ g_{5} \\ 0 \end{bmatrix}$$

$$\dot{S} = \begin{bmatrix} \dot{S}_1 \\ \dot{S}_2 \\ \dot{S}_3 \\ \dot{S}_4 \\ \dot{S}_5 \\ \dot{S}_6 \end{bmatrix} \text{ and } M = \begin{bmatrix} M_{11} & M_{12} & M_{13} & 0 & 0 & 0 \\ M_{21} & M_{22} & M_{23} & 0 & 0 & 0 \\ M_{31} & M_{32} & M_{33} & 0 & M_{35} & 0 \\ 0 & 0 & 0 & M_{44} & 0 & 0 \\ 0 & 0 & 0 & 0 & M_{55} & 0 \\ 0 & 0 & 0 & 0 & 0 & M_{66} \end{bmatrix}$$

Suppose that  $\tau_{sat}$  is computed as

$$\hat{\tau}_{sat} = K.\,sat\left(\frac{S}{\phi}\right)$$

where

$$\hat{\tau}_{sat} = \begin{bmatrix} \widehat{\tau}_{d_{1}s_{1}} \\ \widehat{\tau}_{d_{1}s_{2}} \\ \widehat{\tau}_{d_{1}s_{3}} \\ \widehat{\tau}_{d_{1}s_{4}} \\ \widehat{\tau}_{d_{1}s_{5}} \\ \widehat{\tau}_{d_{1}s_{6}} \end{bmatrix}, K = \begin{bmatrix} K_{1} \\ K_{2} \\ K_{3} \\ K_{4} \\ K_{5} \\ K_{6} \end{bmatrix}, \left( \frac{S}{\emptyset} \right) = \begin{bmatrix} \frac{S_{1}}{\emptyset_{1}} \\ \frac{S_{2}}{\emptyset_{2}} \\ \frac{S_{3}}{\emptyset_{3}} \\ \frac{S_{3}}{\emptyset_{3}} \\ \frac{S_{4}}{\emptyset_{4}} \\ \frac{S_{5}}{\emptyset_{5}} \\ \frac{S_{6}}{\emptyset_{6}} \end{bmatrix} and S = \lambda e + \dot{e}$$

Moreover by replace the formulation (40) in (38) the control output is written as ;

$$\hat{\tau} = \hat{\tau}_{eq} + K. \, sat\left(\frac{S}{\phi}\right) = \begin{cases} \tau_{eq} + K. \, sgn(S) & , |S| \ge \phi \\ \tau_{eq} + K. \, \frac{S}{\phi} & , |S| < \phi \end{cases}$$
((4)

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) Figure 1 shows the position classical sliding mode control for PUMA-560 robot manipulator. By (41) and (39) the sliding mode control of PUMA 560 robot manipulator is calculated as; ( 4

$$\hat{\tau} = \left[M^{-1}(B+C+G) + \dot{S}\right]M + K.\,sat\left(S/\phi\right)$$



FIGURE 1: Block diagram of classical sliding mode controller

## 4. FPGA-BASED SLIDING MODE CONTROLLER

Research on FPGA-based control of systems is considerably growing as their applications such as industrial automation, robotic surgery, and space station's robot arm demand more accuracy, reliability, high performance. For instance, the FPGA-based controls of robot manipulator have been reported in [5-6, 8-13]. Shao and Sun [8, 10]have proposed an adaptive control algorithm based on FPGA for control of SCARA robot manipulator. They are designed this controller into two micro base controller, the linear part controller is implemented in the FPGA and the nonlinear estimation controller is implemented in DSP. Moreover this controller is implemented in a Xilinx-FPGA XC3S400 with the 20 KHz position loop frequency. The FPGA based servo control and inverse kinematics for Mitsubishi RV-M1 micro robot is presented in[9, 11-12] which to reduce the limitation of FPGA capacitance they are used 42 steps finite state machine (FSM) in 840 n second. Meshram and Harkare [5-6] have presented a multipurpose FPGA-based 5 DOF robot manipulator using VHDL coding in Xilinx ISE 11.1. This controller has two most important advantages: easy to implement and flexible. Zeyad Assi Obaid et al. [13] have proposed a digital PID fuzzy logic controller using FPGA for tracking tasks that yields semi-global stability of all closed-loop signals.

The basic information about FPGA has been reported in [4-5, 12-15]. A review of design and implementation of FPGA-based systems has been presented in [4]. The FPGA-based sliding mode control of systems has been reported in [7, 16-18]. Lin et al. [7] have presented low cost and high performance FPGA-based fuzzy sliding mode controller for linear induction motor with 80% of flip flops. The fuzzy inference system has 2 inputs ( $S \& \dot{S}$ ) and one output  $K_f$  with nine rules. Ramos et al. [16] have reported FPGA-based fixed frequency quasi sliding mode control algorithm to control of power inverter. Their proposed controller is implemented in XC4010E-3-PC84 FPGA from XILINX with acceptable experimental and theoretical performance. FPGA-based robust adaptive backstepping sliding mode control for verification of induction motor is reported in [17].

The introduction of language and architecture of Xilinx FPGA such as VHDL or Verilog in sliding mode control of robot manipulator will be investigated in this section. The Xilinx Spartan 3E FPGAs has 5 major blocks: Configurable Logic Blocks (CLBs), standard and high speed Input/output Blocks (IOBs), Block RAM's (BRAMs), Multipliers Blocks, and Digital Clock Managers (DCMs). CLBs is include flexible look up tables (LUTs) to implement memory (storage element) and logic gates. There are 4 slices per CLB each slice has two LUT's. IOB does control the rate of data between input/output pins and the internal logic gates or elements.

It supports bidirectional data with three state operation and multiplicity of signal standards. BRAMs require the data storage including 18-Kbit dual-port blocks. Product two 18-bit binary numbers is done by multiplier blocks. Self-calibrating, digital distributing solution, delaying, multiplying, dividing and phase-shift clock signal are done by DCM [15].

As shown in Figure 1, FPGA based sliding mode controller divided into two main parts: saturation part and equivalent part. To design FPGA based SMC controller using VHDL code, inputs and outputs is played important role. The block diagram of the FPGA-based sliding mode control systems for a robot manipulator is shown in Figure 2. Based on Figure 2 this block (controller) has 9 inputs and 3 outputs. Actual and desired displacements (inputs) are defined as 30 bits and the outputs (teta\_dis) are defines as 35 bits in size. The desired inputs are generated from the operator and send to controllers for calculate the error and applied to sliding mode controller.



FIGURE 2 : RTL FPGA-based controller schematic in XILINX-ISE

To convert float input data to the integer it should be multiply input value by 1000000 and then save these new values in the input files. After the completing simulation, output response should be divided over 1000000 integers to real convert values. But due to simulator (XILINX ISE 9.1) limitations and restrictions on integer data length (32 bits) and it results are 33 bit's words so at the first, controller results is divided over 2 and convert them to the integer part. Therefore the result should be divided over 500000 instead of 1000000. To robot manipulator's FPGA based position sliding mode control, controller is divided into three main sub blocks; Figure 3 shows the VHDL code and RTL schematic in Xilinx ISE software.

The table in Figure 4 indicates the Summary of XA Spartan-3E FPGA Attributes. As mentioned in above, the most significant resources are the LUT's (610 out of 29504), CLB (77 out of 3688), Slice (305 out of 14752), Multipliers (27 out of 36), registers (397), and Block RAM memory (648 K) which there are 4 slices per CLB, each slice has two LUT's. So, Number of 4 input LUTs=610,  $\frac{610}{2} = 305$  slices,  $\frac{305}{4} \cong 77$  CLB's, 610 registers and as a Map report Peak memory usage is 175 MB and registers in the XA3S1600E FPGA.

Moreover the table in Figure 5 illustrates the utilization summary of XA3S1600E-spartan.

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FIGURE 3: Design RTL FPGA-based SMC using XILINX-ISE

		Equivalent	(	CLB One CLB =	Array Four Slic	es)		Block				Maximum	
Device	System Gates	Logic Cells	Rows	Columns	Total CLBs	Total Slices	Distributed RAM bits <sup>(1)</sup>	RAM bits <sup>(1)</sup>	Dedicated Multipliers	DCMs	Maximum User I/O	Differential I/O Pairs	
XA3S100E	100K	2,160	22	16	240	960	15K	72K	4	2	108	40	
XA3S250E	250K	5,508	34	26	612	2,448	38K	216K	12	4	172	68	
XA3S500E	500K	10,476	46	34	1,164	4,656	73K	360K	20	4	190	77	
XA3S1200E	1200K	19,512	60	46	2,168	8,672	136K	504K	28	8	304	124	
XA3S1600E	1600K	33,192	<mark>76</mark>	<mark>58</mark>	3,688	<mark>14,752</mark>	231K	648K	<mark>36</mark>	8	376	<mark>156</mark>	

Notes:

1. By convention, one Kb is equivalent to 1,024 bits.

FIGURE 4: Summary of XA Spartan-3E FPGA attributes

## 5. **RESULTS**

PD Matlab-based sliding mode controller (PD-SMC) and PID Matlab-based sliding mode control ler (PID-SMC) and FPGA-based sliding mode controller were tested to Step response trajectory. In this simulation the first, second, and third joints are moved from home to final position without and with external disturbance. The simulation was implemented in Matlab/Simulink and Xilinx-ISE 9.1 environments. Trajectory performance, torque performance, disturbance rejection, steady state error and RMS error are compared in these controllers. It is noted that, these systems are tested by band limited white noise with a predefined 40% of

relative to the input signal amplitude which the sample time is equal to 0.1. This type of noise is used to external disturbance in continuous and hybrid systems.

	Device	e Utilization Summary		
Logic Utilization	Used	Available	Utilization	Note(s)
Number of Slice Flip Flops	216	29,504	1%	
Number of 4 input LUTs	610	29,504	2%	
Logic Distribution				
Number of occupied Slices	342	14,752	2%	
Number of Slices containing only related logic	342	342	100%	
Number of Slices containing unrelated logic	0	342	0%	
Total Number of 4 input LUTs	622	29,504	2%	
Number used as logic	610			
Number used as a route-thru	12			
Number of bonded IOBs	288	376	76%	
IOB Flip Flops	181			
Number of GCLKs	2	24	8%	
Number of MULT18×18SIOs	27	36	75%	
Total equivalent gate count for design	10,334			
Additional JTAG gate count for IOBs	13,824			

Figure 5

XA3S1600E device utilization summaries



FIGURE 6 : Step PD-SMC and PID-SMC for first, second and third link trajectory without any disturbance.

By comparing step response, Figure 6, in PD and PID-SMC, conversely the PID's overshoot (0%) is lower than PD's (1%), the PD's rise time (0.483 Sec) is dramatically lower than PID's (0.9 Sec); in addition the Settling time in PD (Settling time=0.65 Sec) is fairly lower than PID (Settling time=1.4 Sec).

**Disturbance rejection:** Figure 7 is indicated the power disturbance removal in PD and PID-SMC. As mentioned before, SMC is one of the most important robust nonlinear controllers. Besides a band limited white noise with predefined of 40% the power of input signal is applied to the step PD and PID-SMC; it found slight oscillations in trajectory responses.



**FIGURE 7:** Step PD SMC and PID SMC for first, second and third link trajectory with external disturbance.

Among above graph, relating to step trajectory following with external disturbance, PID and PD SMC have slightly fluctuations. By comparing overshoot, rise time, and settling time; PID's overshoot (0.9%) is lower than PD's (1.1%), PD's rise time (0.48 sec) is considerably lower than PID's (0.9 sec) and finally the Settling time in PD (Settling time=0.65 Sec) is quite lower than PID (Settling time=1.5 Sec).

**Chattering phenomenon:** As mentioned in previous section, chattering is one of the most important challenges in sliding mode controller which one of the major objectives in this research is reduce or remove the chattering in system's output. Figure 8 has shown the power of boundary layer (saturation) method to reduce the chattering in PD-SMC.



**FIGURE 8** : PD-SMC boundary layer methods Vs. PD-SMC with discontinuous (Sign) function

Figure 9 has indicated the power of chattering rejection in PD and PID-SMC, with and without disturbance. As mentioned before, chattering can caused to the hitting in driver and mechanical parts so reduce the chattering is more important. Furthermore band limited white noise with predefined of 40% the power of input signal is applied the step PD and PID-SMC, it seen that the slight oscillations in third joint trajectory responses. Overall in this research with regard to the step response, PD-SMC has the steady chattering compared to the PID-SMC.



**FIGURE 9 :** Step PID SMC and PD SMC for first, second and third link chattering without and with disturbance.

**Errors in the model:** Figure 10 has shown the error disturbance in PD and PID SMC. The controllers with no external disturbances have the same error response, but PID SMC has the better steady state error. By comparing steady and RMS error in a system with no disturbance it found that the PID's errors (Steady State error = 0 and RMS error=1e-8) are approximately less than PD's (Steady State error  $\cong 1e - 6$  and RMS error=1.2e - 6).

Figure 10 shows that in first seconds; PID SMC and PD SMC are increasing very fast. By comparing the steady state error and RMS error it found that the PID's errors (Steady State error = -0.0007 and RMS error=0.0008) are fairly less than PD's (Steady State error  $\approx$  0.0012 and RMS error=0.0018), When disturbance is applied to PD and PID SMC the errors are about 13% growth.



**FIGURE 10** : Step PID SMC and PD SMC for first, second and third link steady state error performance.

### 5.2 FPGA-Based Sliding Mode Controller

**Timing Detail:** As a simulation result in XILINX-ISE 9.1, it found that this controller is able to make as a fast response at 15.716 ns with 63.29 MHz of a maximum frequency. From investigation and synthesis summary, this design has 15.716 ns delay to each controller for 46 logic elements and also the offset before CLOCK is 55.773 ns for 132 logic gates. Figures 11 to 13 have indicated the displacement, error performance, teta discontinuous (torque performance) at different time.

As shown in Figure 11 the controller gives action at 6  $\mu$ s as a result before this time all signals and error equal to zeros.

		60									
Current Simulation Time: 31 us		p ,	10 	20	1						
o, I clk	1	inannun an			mmmmm						
sample_clk	0										
actual_d	0	0			5000000						
actual_d	0	( 0			5000000						
dsired_d	0	0.	k	5000000							
E St desired	0	0	X	5000000							
error[39:0]	0	0			0						
E Sterror_di	0	0			0						
E St error_ga	0	0			0						
🖬 🔂 s[39:0]	0	0			0						
k1_mul_s	0	K		0							
k2_mul_s	0	0			0						
E Steta_dis	0	0			0						
E Steta_dis	0	0			-1053318						
E Steta_dis	0	0			0						
E Steta_dis	0	0			0						

FIGURE 11 : Timing diagram using Xilinx ISE 9.1 of the FPGA-based SMC before running

In Figure 12 at 6.5  $\mu$ s (transient response) the response has a large steady state error, 3.92, the desired displacement is 5, the actual displacement is 1.6 and the torque performance is 256.9 N.m.



FIGURE 12: Step PD SMC for first, second and third link for desired and actual inputs, error performance, and torque performance at 6.5  $\mu s$ 

Figure 13 has shown the PD-SMC at  $t=100\mu s$  (steady state response), at this time the steady State error is equal to zero, the desired displacement is 5, the actual displacement is 5 and the torque performance is 1.005 Nm.

Current Simulation		0 100.0 0 100 2	00
Time: 541 us			Ĺ
õ, cik	0	XIXIIIIIIIIIIIXXXXXXXXXXXXXXXXXXXXXXXX	$\square$
🍓 🛛 sample_cik	1		$\underline{D}$
🖬 🕅 actual_displacement[29:0]	5000000	0 5000000	
🗖 🖏 actual_dis_buf(39:0)	5000000	0	
🖬 駴 dsired_displacement[29:0]	5000000	0 5000000	
🖬 🖏 desired_dis_buf[39:0]	5000000	0X 5000000	
🖬 👼 error[39:0]	0		
🖬 🚮 error_diff[39:0]	0		
🖬 🖏 error_gain[45:0]	0	0	
🖬 🔂 s[39:0]	0		
k1_mul_satured_s[45:0]	0	0	
🖬 🔂 k2_mul_s[45:0]	0		
🖬 🖏 teta_dis_buf(39:0)	0		
🖬 🔂 teta_dis1(34:0)	-1053318	-1053318	
🖬 🔂 teta_dis2[34:0]	0		
🖬 😽 teta_dis3[34:0]	0		

FIGURE 13 : Step PD SMC for first, second and third link for desired and actual inputs, error performance, and torque performance in  $100\mu s$ .

Figure 14 shows the delay with the robot manipulator affects the beginning of the response. Consequently the delay for this system is equal to  $0.1\mu s$ .

										6050.	9				100						615	50.0					
Current Simulation Time: 31000 ms			6000					6050	1		12	6100					6150				6						
ol cik	0			Π	П		П				Π	П		Π	П	Т	Т	t	Т	П	Г			П	t	U	Т
al sample_clk	0							1										-							-		
🖬 💏 actual_d	5000000					0	1			X					-111	110									-15	1560	
actual_d	5000000			ac	tua	il di	ispla	icen	nent	Г	U		-	-	-	-	_	-	_	_		1			-11	1110	
🖬 💏 dsired_d	5000000			de	esir	ed	disp	lace	emer	nt					-			50									
E St desired	5000000						P				0														500		
■ 😽 error[39:0]	0	-									0		du	elay	v=1	00	ns								511	1110	
Berror_di	40%0000000000							-			0		-					-				TX			-51	11110	
E St error_ga	467000000000000										0		100												3066		
🖬 😽 s[39:0]	40%0000000000										0				-							bχ			81	m	
84 k1_mul_s	467000000000000000000000000000000000000												-	0											-		
k2_mul_s	46%000000000000										0							-				ΣX			245	3332	
B St teta_dis	0										0				-							sχ			245	1332	800
E Steta_dis	-1053318										0		-					-				5X			245	13328	800
B St teta_dis	0										0		-					-				5X			245	3332	
🛛 😽 teta_dis	0										0											X			245	3332	900

FIGURE 14 : The delay time in PD-SMC between desired displacement and actual displacement

Figures 15 and 16 show the chattering in FPGA-based SMC. In Figure 15, the chattering analysis from  $6.2 \,\mu s$  to  $7 \mu s$ . It can be seen that the chattering is eliminated in this design.

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Current Simulation Time: 31000 ns		6200	6400		6600		6800		7000						
olik	0					Intelligitettettettettettettettettettettettettet									
all sample_clk	1								1						
Actual_displace	-151560	-151560 710888	X 1607462 )	2299841	2903755	3421582	3835591	4151869	4385778						
actual_dis_but[39:0]	-111110	-111110 X -151560	X 710888 )	1607462	2299841	2903755	3421582	3835591	( 4151869 )						
dsired_displace	5000000	5000000													
desired_dis_but[39:0]	5000000	500000													
error[39:0]	5111110	5111110 \$ 5151560	X 4289112 )	3392538	2700159	2096245	1578418	1154409	( 848131 X						
error_diff[39:0]	40h000308E4BC	51111100 X 404500	X -8624480	-8965740	-6923790	-6039140	-5178270	-4140090	X -3162780 X						
error_gain(45:0)	46h000001D3EFA4	30666660 X 30909360	X 25734672 )	20355228	16200954	12577470	9470508	6986454	5088786 X						
🗖 😽 s[39:0]	40h0004DFD460	81777760 31313860	17110192	11389488	9277164	6538330	4292238	2846364	1926006						
k1_mul_satured	46h0000000000000					)		A design of the second s							
k2_mul_s[45:0]	46h0000923AE340	2453332800 93941580	513305760	341684640	(278314920)	(196149900)	(128767140)	85390920	57780180						
teta_dis_buf[39:0]	2453332800	2453332800 93941580	513305760	341684640	278314920	196149900	128767140	85390920	57780180						
C 84 teta_dis1[34:0]	2454332800	2454332800 75636124	X 533195860	256986160	( 95097520 )	73497280	55214680	( 19350820 )	14517672						
teta_dis2[34:0]	2453332800	2453332800 93941580	513305760	341684640	278314920	196149900	(128767140)	85390920	57780180						
teta_dis3[34:0]	2453332800	2453332800 93941580	513305760	341684640	(278314920)	196149900	128767140	85390920	57780180						
		Analysis t	he chatte	ring from	n 6.2 us i	to 7 us									

**FIGURE 15:** Chattering rejections in FPGA-based SMC (from 6.2  $\mu s$  to  $7\mu s$ )

Figure 16 shows the power of chattering rejections in FPGA-based SMC, it found that this design is eliminated the chattering in certain situation as well as Matlab-based PD SMC.

Current Simulation Time: 31000 ns		26000		26200	5200 26400			1		26600		26800				
õ, cik	0						mmm		nnnnn	10000				Innin		
isample_clk	1				1						1	1				
🗖 🎀 actual_displace	5000000							5000000								
🖬 😽 actual_dis_but(39:0)	5000000	1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 - 1000 -						5000000			2					
dsired_displace	5000000							5000000								
desired_dis_buf[39:0]	5000000							5000000								
error[39:0]	0	5	1		10			0.	- 10				1			
error_dif[39:0]	40%0000000000							0								
error_gain[45:0]	46%0000000000000	1				1.5		0				- 20				
🖬 😽 s[39:0]	40%0000000000							0			1					
k1_mul_satured	46%0000000000000							0								
k2_mul_s[45:0]	46%000000000000	1		2				0				100				
E 🛃 teta_dis_but[39:0]	0	-						0								
teta_dis1[34:0]	34358685050	2		8		76-0	23	435868505	0							
teta_dis2[34:0]	0	1						0								
teta_dis3[34:0]	0	1						0								
		Ana	alysis t	he chat	tering	from	26 us	to 26.9	us							

**FIGURE 16:** Chattering rejections in FPGA-based SMC (from  $26 \ \mu s$  to  $26.9 \ \mu s$ )

The best possible coefficients in Step FPGA-based PD-SMC are;  $K_p = 000001 = 1, K_v = 011110 = 30$ ,  $\phi_1 = \phi_2 = \phi_3 = 000001 = 1$ , and  $\lambda_1 = \lambda_2 = \lambda_3 = 000110 = 6$ . By comparing some control parameters such as overshoot, rise time, settling time and steady state error in Matlab-based PD-SMC, FPGA-based PD-SMC; overshoot (**PD-SMC=1% and FPGA-SMC=0.005%**), rise time (**PD-SMC=0.4 sec and FPGA-SMC8.2**  $\mu$  s), settling time (**PD-SMC=0.4 sec and FPGA-SMC8.2**  $\mu$  s), settling time (**PD-SMC=0.4 sec and FPGA-SMC=0.0003 and FPGA-SMC=0**) consequently it found that in fast response, the FPGA based-SMC's parameter has the high-quality performance.

## 6. CONCLUSION

Refer to the research, a position FPGA-based sliding mode control design and application to robot manipulator has proposed in order to design high performance nonlinear controller in the

presence of certainties. Regarding to the positive points in sliding mode controller and FPGA the output has improved. Sliding mode controller by adding to the FPGA single chip IC has covered negative points. Obviously PUMA 560 robot manipulator is nonlinear so this paper focuses on comparison between Matlab-based sliding mode controller and FPGA-based sliding mode controller, to opt for mobility control method for the industrial manipulator.

Higher implementation speed and small chip size versus an acceptable performance is reached by designing FPGA-based sliding mode controller. This implementation considerably reduces the chattering phenomenon and error in the presence of certainties. The controller works with a maximum clock frequency of 63.29 MHz and the computation time (delay in activation) of this controller is  $0.1\mu s$ . As a result, this controller will be able to control a wide range of robot manipulators with a high sampling rates because it's small size versus high speed markets.

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